

ABSTRACT OF THE DISCLOSURE

An active matrix display device for suppressing voltage variation ΔV due to off-operation of a gate pulse, including TFTs and picture-element electrodes, at least one of the TFTs being assigned to each picture element, and each of the TFTs having a gate electrode connected to a gate line (first gate line), and a source and a drain one of which is connected to a data line, wherein a picture-element electrode concerned is formed so as to be overlapped with the first gate line through an insulator, and also so as to be overlapped through an insulator with a gate line other than the first gate line or a wiring disposed in parallel to the first gate line.